

ABSTRACT

The present invention provides an array substrate in which delay in signal transmission is reduced and provides a display device in which superior display quality is achieved, by using wirings of low resistivity, and moreover, by suppressing increase in wiring resistance caused by contact resistance. A display area in which pixel electrodes (5) are formed, a gate line (2) arranged between the pixel electrodes, a data line (4) crossing over the gate line, a terminal to which a scanning signal for the gate line is applied, an extended scanning line (14), formed from a conductive film of different layer from that for the gate line, for connecting the collected auxiliary capacitance line with the terminal, an auxiliary capacitance line (11) arranged in parallel to the gate line, a collected auxiliary capacitance line (13) arranged in parallel to the signal line and electrically connected to the auxiliary capacitance line, a terminal to which a common signal is applied, and an extended auxiliary capacitance line (15), formed from a conductive film of different layer from that for the collected auxiliary capacitance line, for connecting the collected auxiliary capacitance line with the terminal are formed.

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